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TRANSMITTAL FORM

(to be used for all correspondence after initial filing) Total Number of Pages in This Submission

Application Number	10/072,931
Filing Date	February 12, 2002
First Named Inventor	Shunpei YAMAZAKI et al.
Group Art Unit	2812
Examiner Name	S. Isaac
Attorney Docket Number	0756-2344

ENCLOSURES (check all that apply)							
Fee Transmittal Form Fee Attached Amendment / Reply After Final Affidavits/declaration(s Extension of Time Reques Express Abandonment Re Information Disclosure Sta Certified Copy of Priority Document(s) Response to Missing Parts Incomplete Application Response to Missing Farts under 37 CFR 1.52 or	equest atement		After Allowance Communication to Group Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, ReptyBrief) Proprietary Information Status Letter Other Enclosures 1. 2. 3. 4. 5. 6. 2800 hereby authorized to charge any additional overpayments to Deposit Account No. 50-ed docket number.				
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT							
Firm or Individual name	Robinson PMB 955 21010 Sou	binson, Reg. No. 38,285 Intellectual Property Law Office, uthbank Street Falls, VA 20165	P.C.				
Signature	25	·					
Date .	March 19,	2003					
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Type or printed name	Adele M. Stamper		
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PTO/SB/17 (11-00) Approved for use through 10/31/2002. OMB 0651-0032

Complete if Known

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Name (Print/Type)	Eric J. Rodinson			rney/Ag		٥٥,،	ده.	Telephone		
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In reseatent A	Application of)	Art Unit: 2812
In reseatent A	1AZAKI et al.)	Examiner: S. I
Serial No.	10/072,931) [CER
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CERTIFICATE OF MAILING

Isaac

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Name: Adele M. Stamper

METHOD OF MANUFACTURING)
A SEMICONDUCTOR DEVICE)

AMENDMENT & RESPONSE TO RESTRICTION REQUIREMENT

Honorable Commissioner of Patents

Washington, D.C. 20231

Sir:

In response to the Office Action of February 19, 2003, please amend the subject application as follows:

IN THE CLAIMS:

Please amend claims 47, 66, and 67 as follows:

47. (Amended) A method of manufacturing a semiconductor Sdevice comprising:

forming a first semiconductor film having an amorphous structure over a substrate;

providing the first semiconductor film with a material for promoting crystallization; heating the first semiconductor film for crystallizing;

irradiating the first semiconductor film with a laser light for improving crystallinity;

forming a barrier layer over the first semiconductor film having crystalline structure:

forming a second semiconductor film over the barrier layer;

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